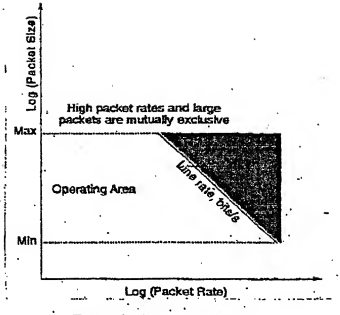


*Figure 1 Packet rate vs. packet size*

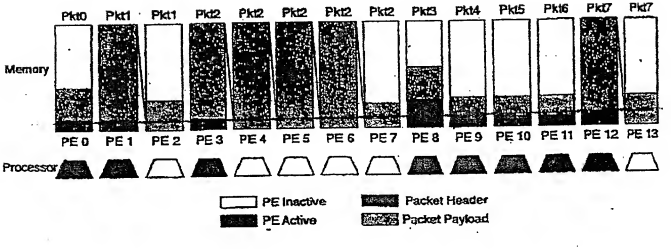


*Figure 2 Allocation of packets to Processing Elements*

20112007 "0 2948 7001"

20041201.0214.02

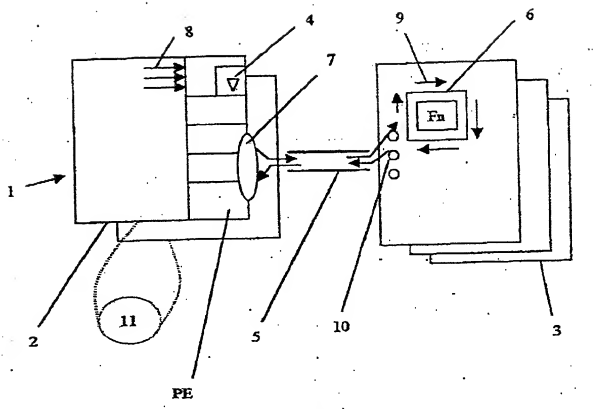


Figure 3

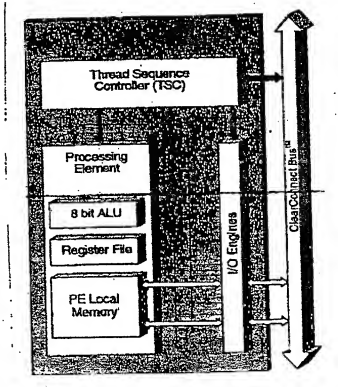


Figure 4 Structure of MTAP processor.

20h120" 8h6E200T

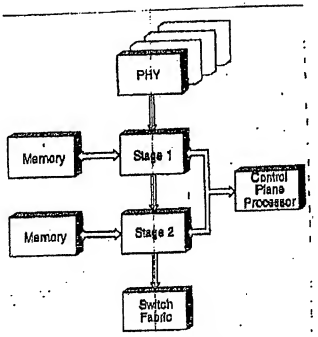


*Figure 5 Example ingress pipeline*

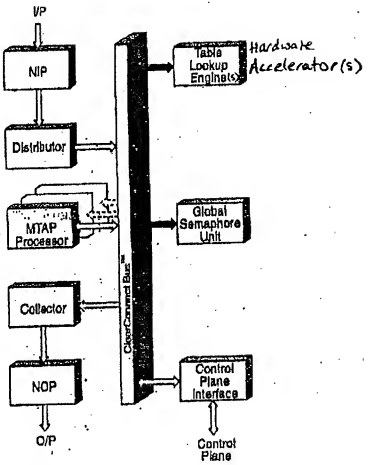


*Figure 6 Example fast path processing subsystem*

20nT20' 3h6E200T

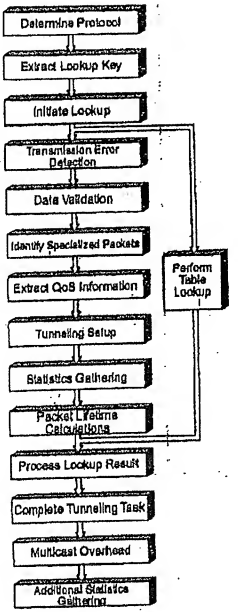


*Figure 8 Overlap of packet processing and table lookup*

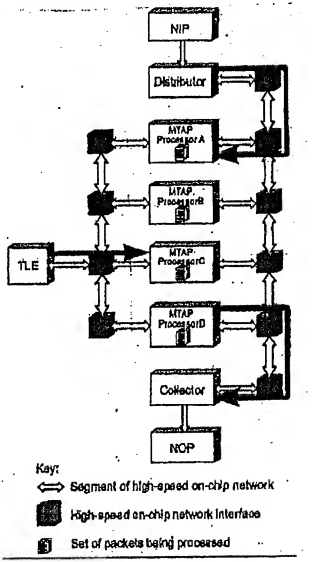


*Figure 9 Snapshot of system behavior*

20nT20" SN6E/00T

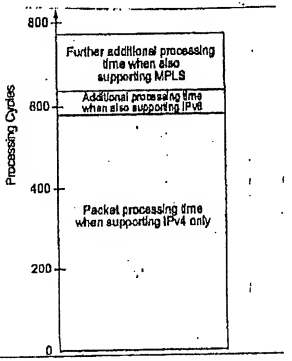


Figure8 Cycle expenditure for multiple protocol support

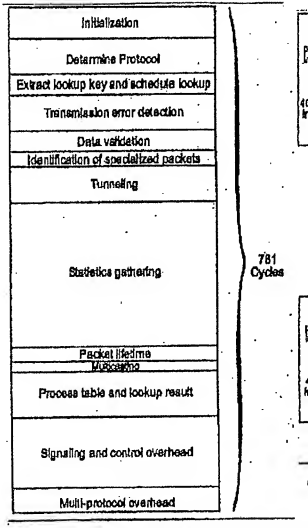


Figure11 Cycle expenditure by function

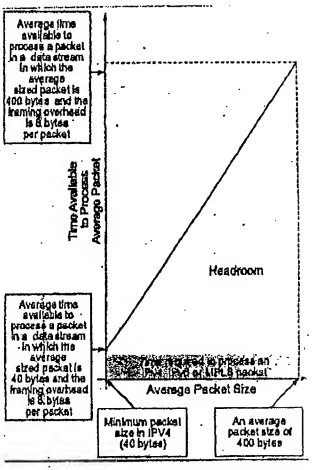


Figure12 Headroom as a function of average packet size

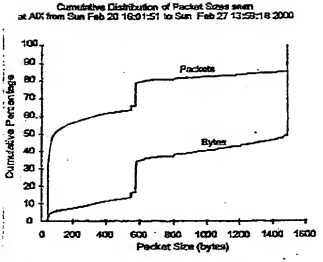


Figure 7 A real world traffic profile.

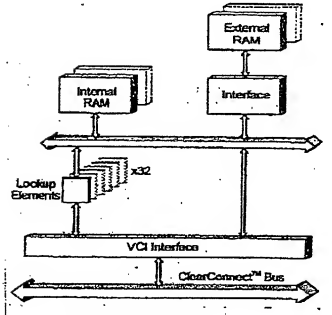


Figure 14 An example TLE

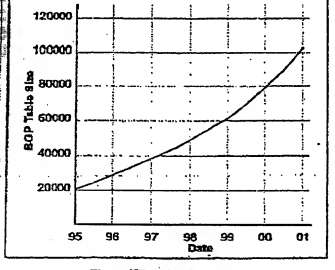


Figure 13 BGP Table Size

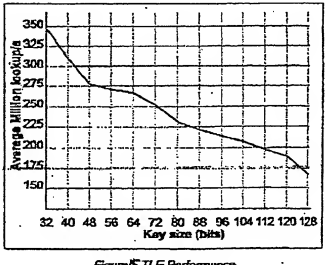


Figure 15 TLE Performance